Abstracts

Circuit Design to Reduce 3rd Order Intermodulation Distortion in FET Amplifiers

R.J. Gilmore, R. Kiehne and F.J. Rosenbaum. "Circuit Design to Reduce 3rd Order Intermodulation Distortion in FET Amplifiers." 1985 MTT-S International Microwave Symposium Digest 85.1 (1985 [MWSYM]): 413-416.

Using a large-signal computer model for the MESFET with a modified harmonic balance technique the third-order intermodulation response of general amplifier circuits is found. The effect of device, bias, and impedance changes is investigated and compared with experimental results for a single-stage feedback amplifier. The method is useful for the design of microwave linearizes. A novel scheme for the reduction of intermodulation distortion in power amplifiers operating near compression is presented with experimental data.

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